

**Notice of References Cited**

Application/Control No.

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Applicant(s)/Patent Under  
Reexamination  
BAECKLER, GREGG

Examiner

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Art Unit

2128

Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	C	US-5,748,488	05-1998	Gregory et al.	716/18
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Cong et al. "Cut Ranking and Pruning: Enabling a General and Efficient FPGA Mapping Solution", 1999, ACM, pgs 29-35
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.